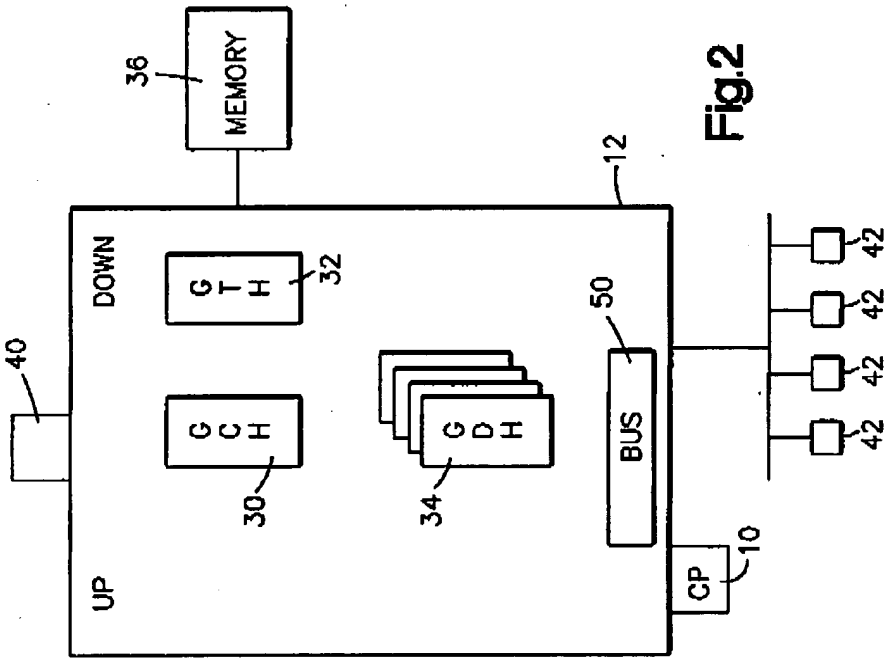

36
MEMORY
12
DOWN
G T H
32
40
G C H
30
G D H
34
50
BUS
42
42
42
42
UP
CP
10

Fig.2

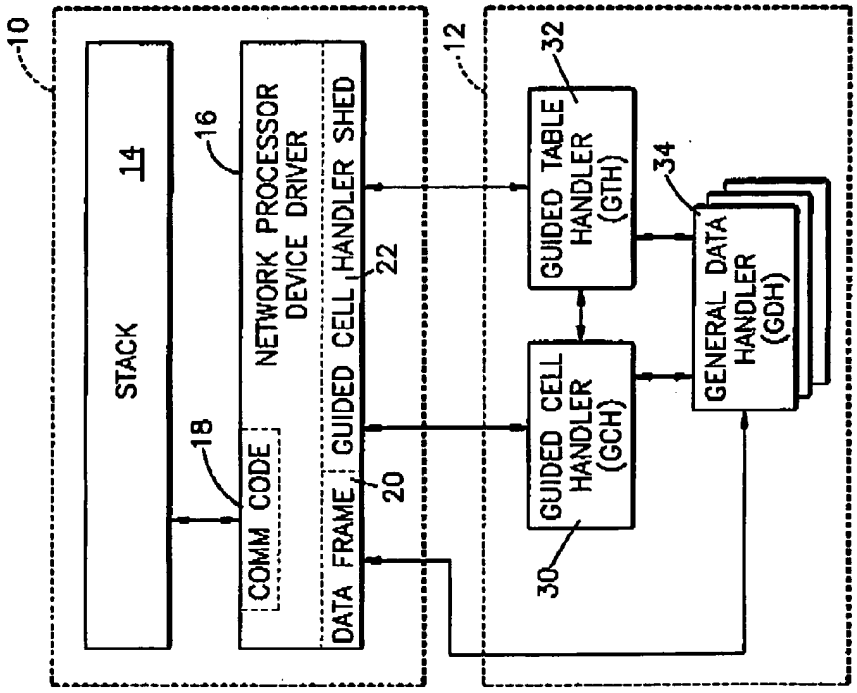

10
STACK
14
16
NETWORK PROCESSOR DEVICE DRIVER
18
COMM CODE
GUIDED CELL HANDLER SHED
22
DATA FRAME
20
12
32
GUIDED TABLE HANDLER (GTH)
34
GENERAL DATA HANDLER (GDH)
GUIDED CELL HANDLER (GCH)
30

Fig.1

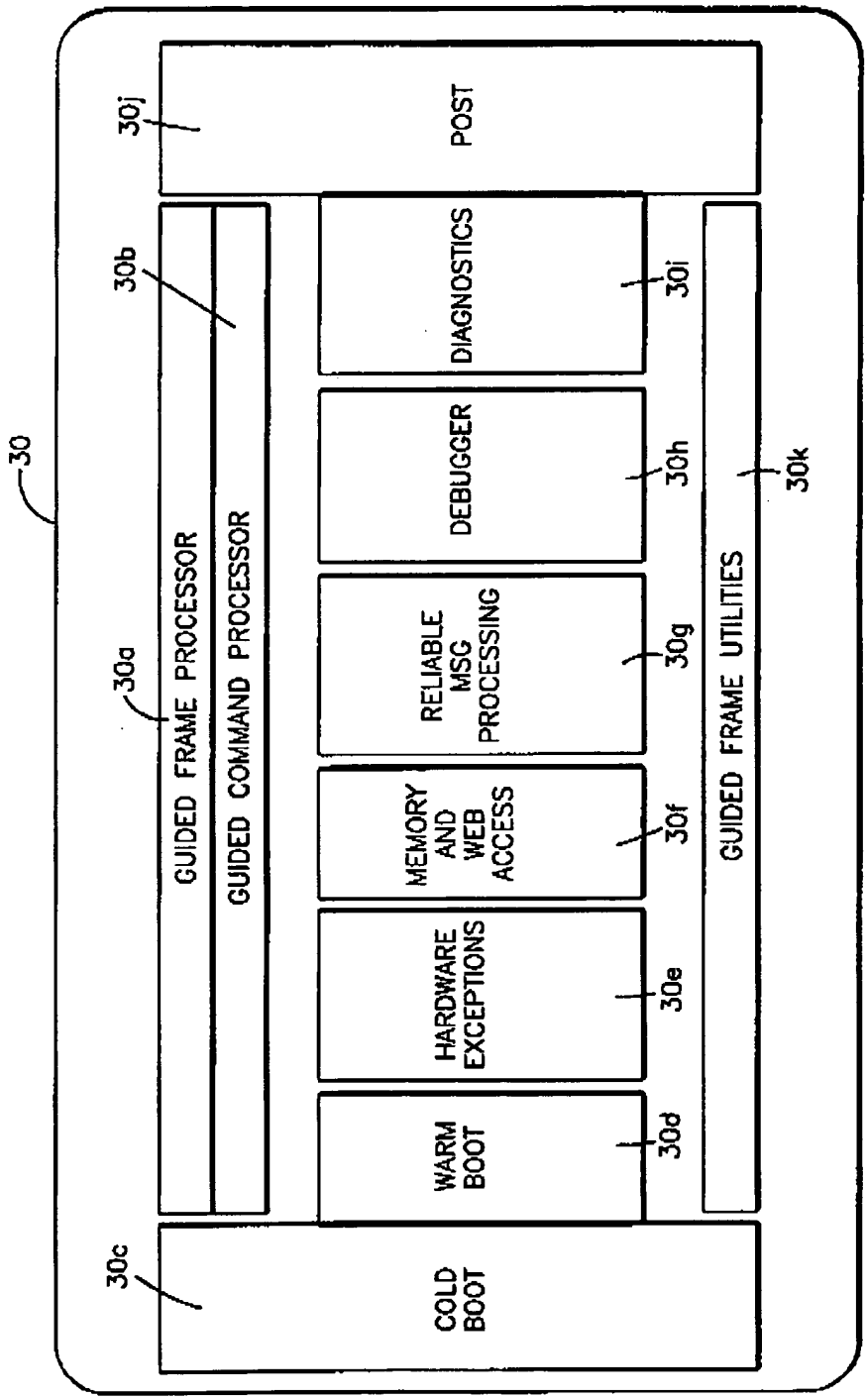
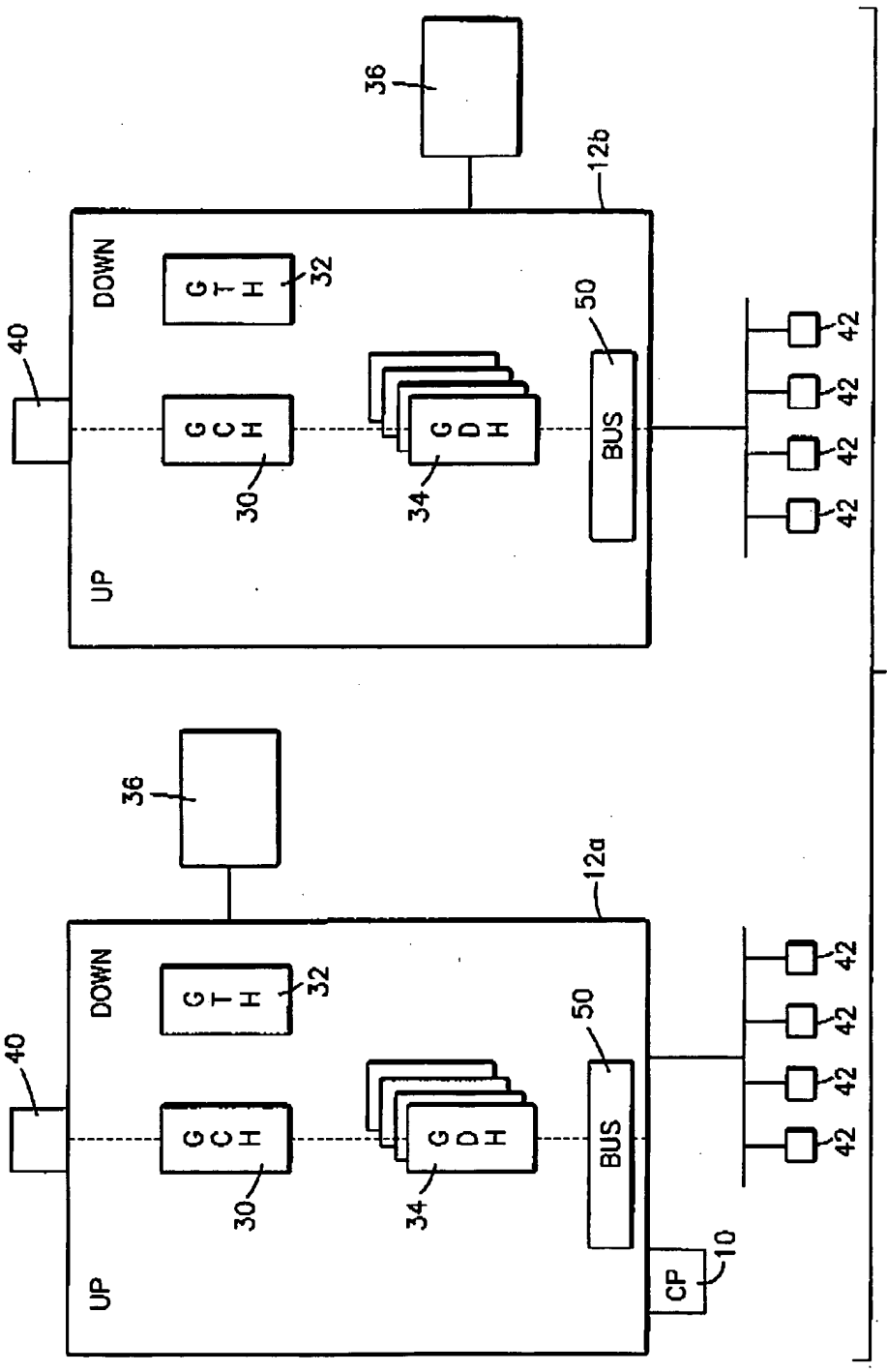

30
30j
POST
30b
GUIDED FRAME PROCESSOR
GUIDED COMMAND PROCESSOR
30a
DIAGNOSTICS
30i
DEBUGGER
30h
RELIABLE MSG PROCESSING
30g
MEMORY AND WEB ACCESS
30f
HARDWARE EXCEPTIONS
30e
WARM BOOT
30d
COLD BOOT
30c
GUIDED FRAME UTILITIES
30k

Fig.2a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12b
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GCH
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GDH
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50
BUS
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42
42
UP
36
12a
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GTH
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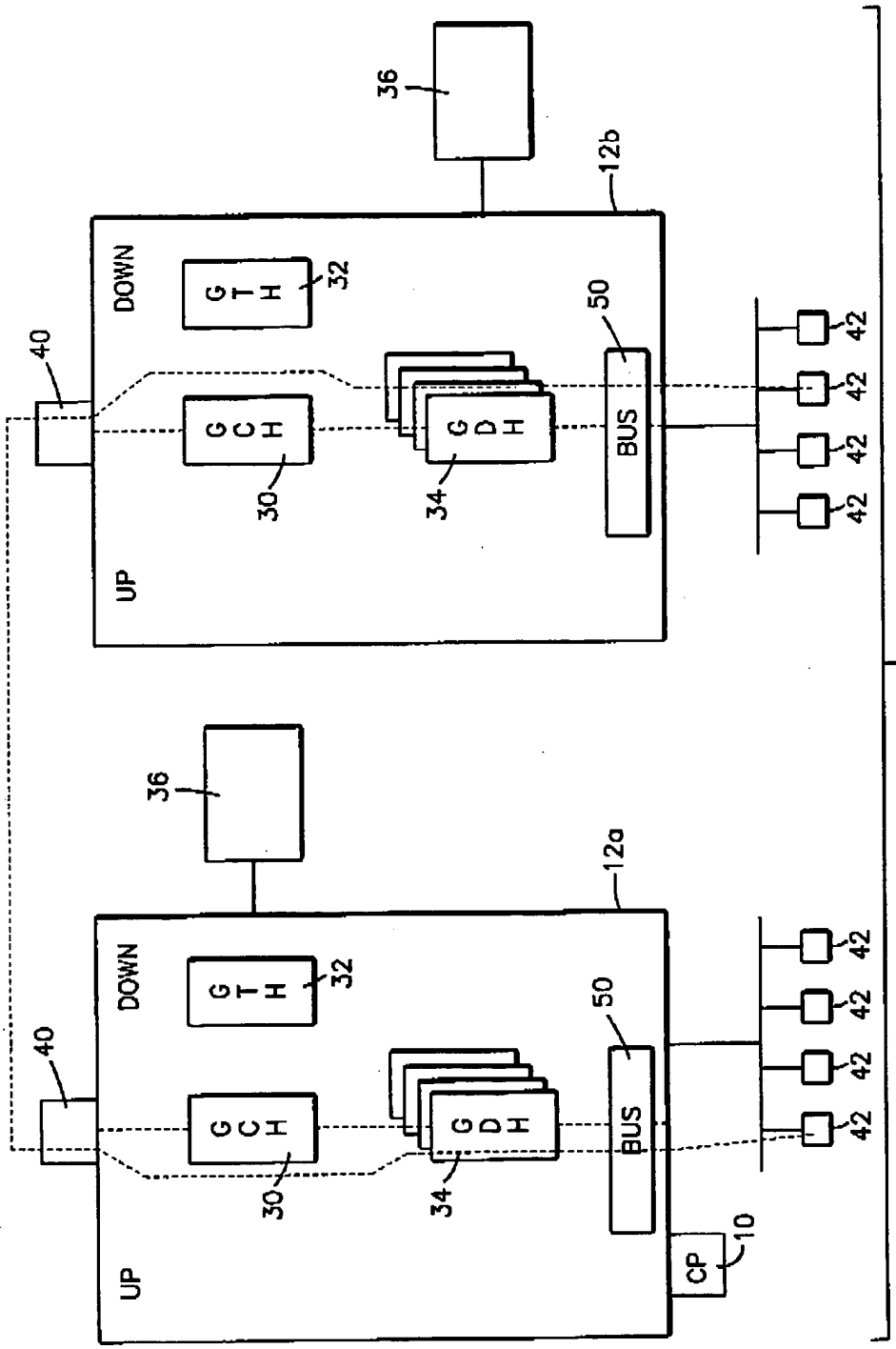
40
GCH
30
GDH
34
50
BUS
42
42
42
42
CP
10
UP

Fig.3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12b
DOWN
G T H
32
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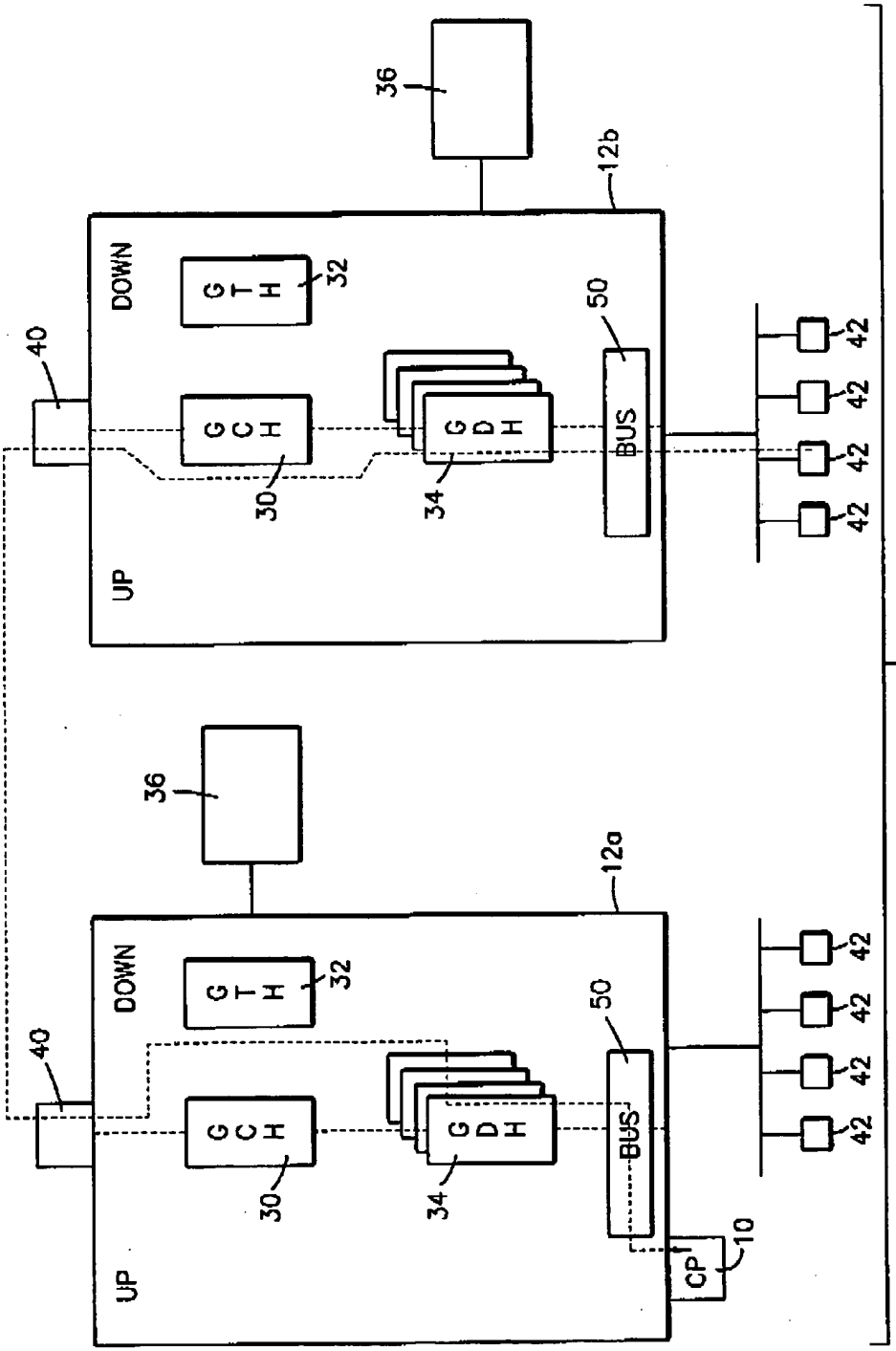
40
G C H
G D H
BUS
42
42
42
42
30
34
UP
36
12a
DOWN
G T H
32
50
40
G C H
G D H
BUS
42
42
42
42
30
34
CP
10
UP

Fig.4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12b
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GTH
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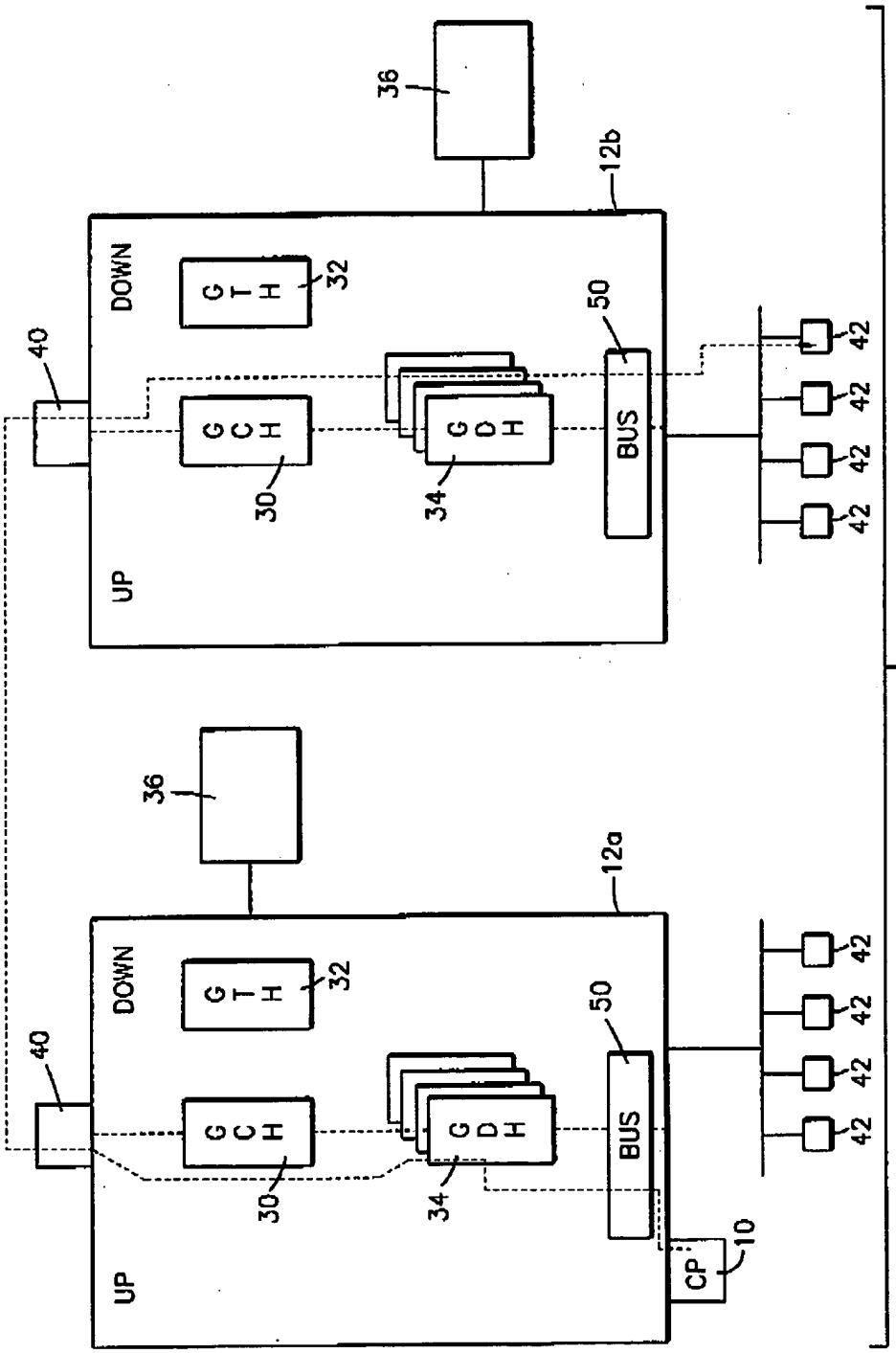
GCH
GDH
BUS
42
42
42
42
30
34
UP
36
12a
DOWN
GTH
32
50
40
GCH
GDH
BUS
42
42
42
42
30
34
CP
10
UP

Fig.5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GCH
GDH
BUS
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34
UP
36
12a
DOWN
GTH
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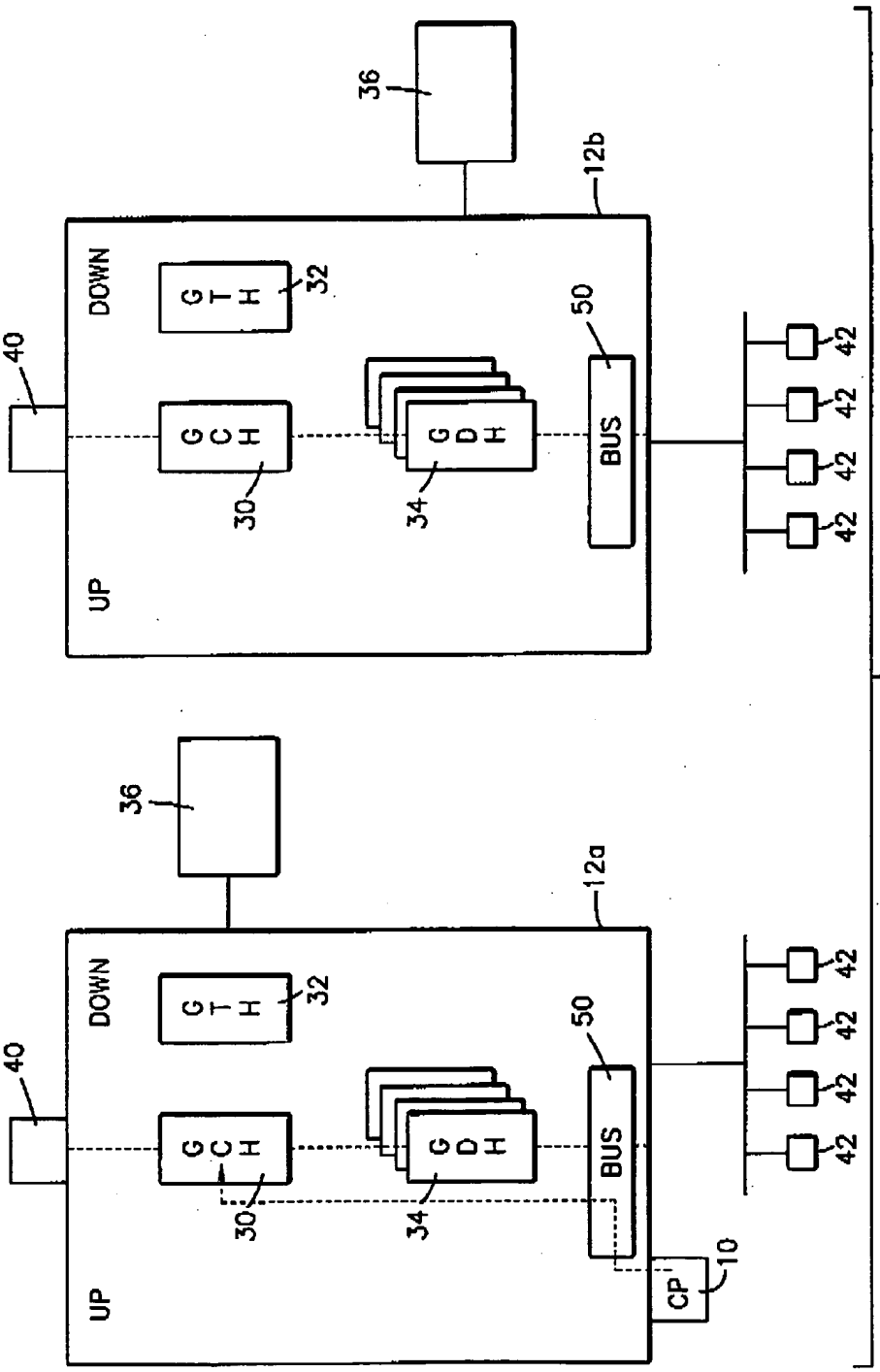
GCH
GDH
BUS
42
42
42
42
42
30
34
CP
10
UP

Fig.6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GTH
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GCH
GDH
BUS
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42
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34
UP
36
12a
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GTH
32
50
40
GCH
GDH
BUS
42
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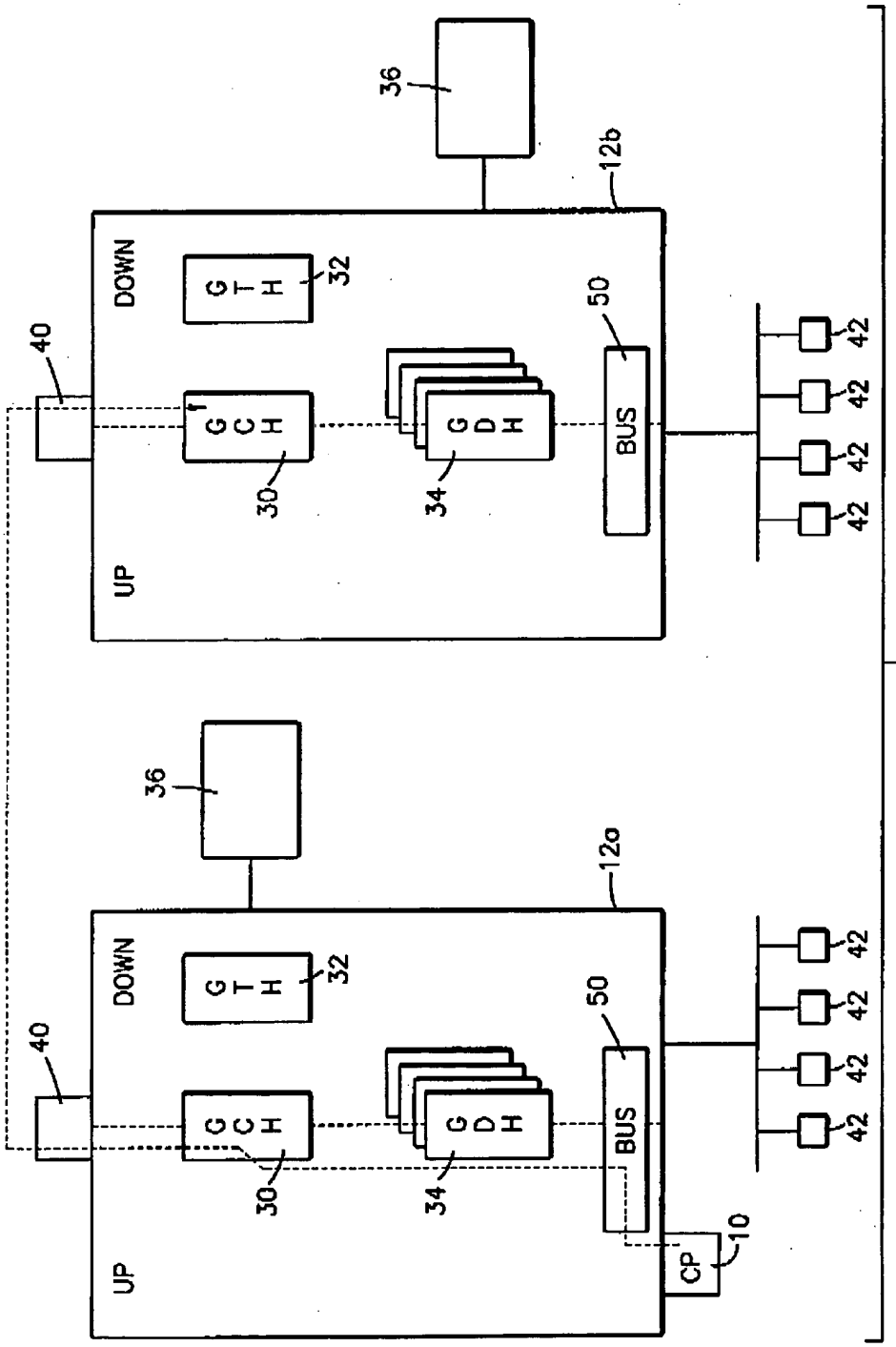
42
30
34
CP
10
UP

Fig.7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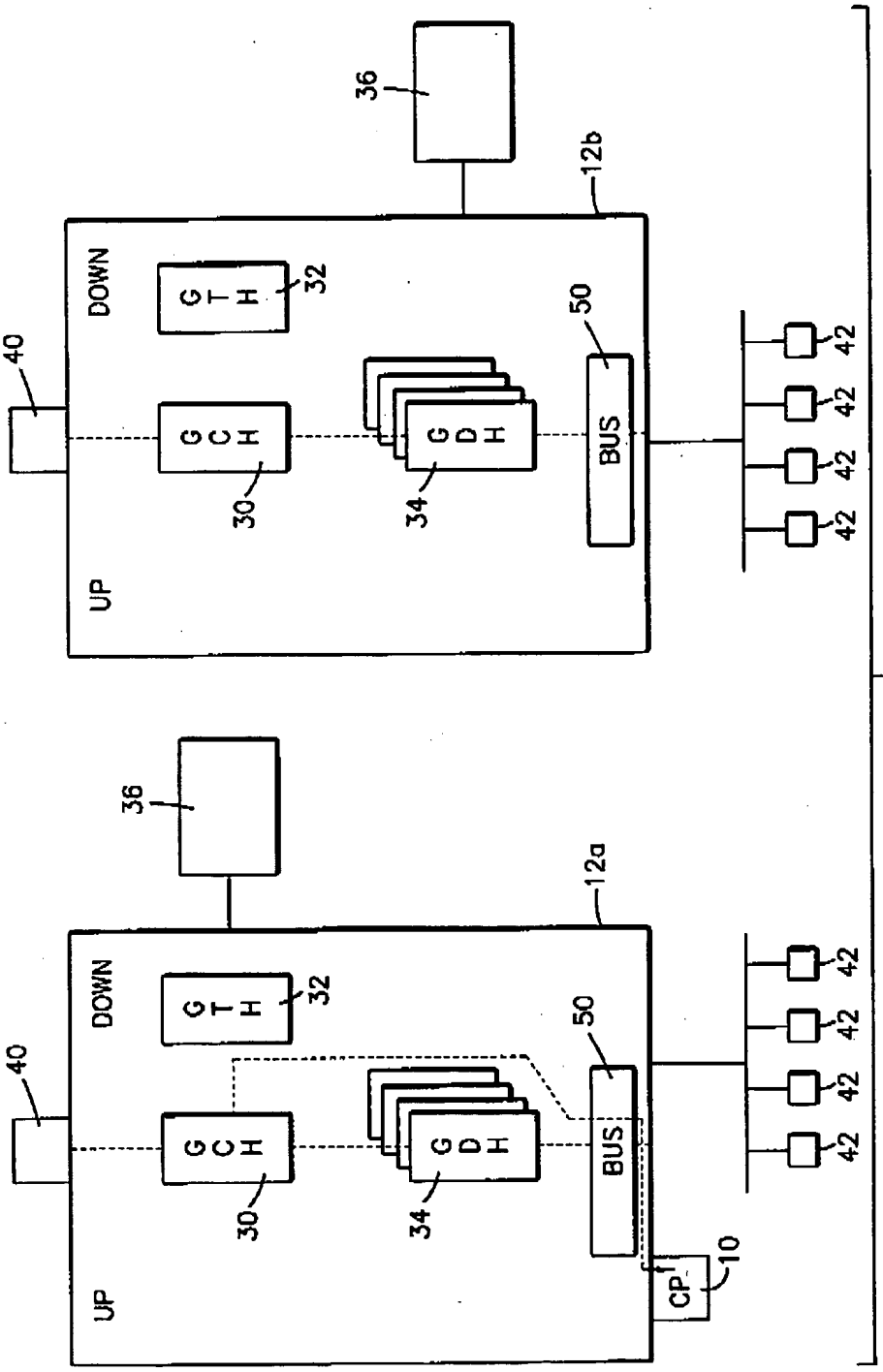
42
GCH
GDH
BUS
30
34
UP
36
12a
DOWN
GTH
32
50
40
42
42
42
42
GCH
GDH
BUS
30
34
CP
10
UP

Fig.8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GTH
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40
GCH
GDH
BUS
42
42
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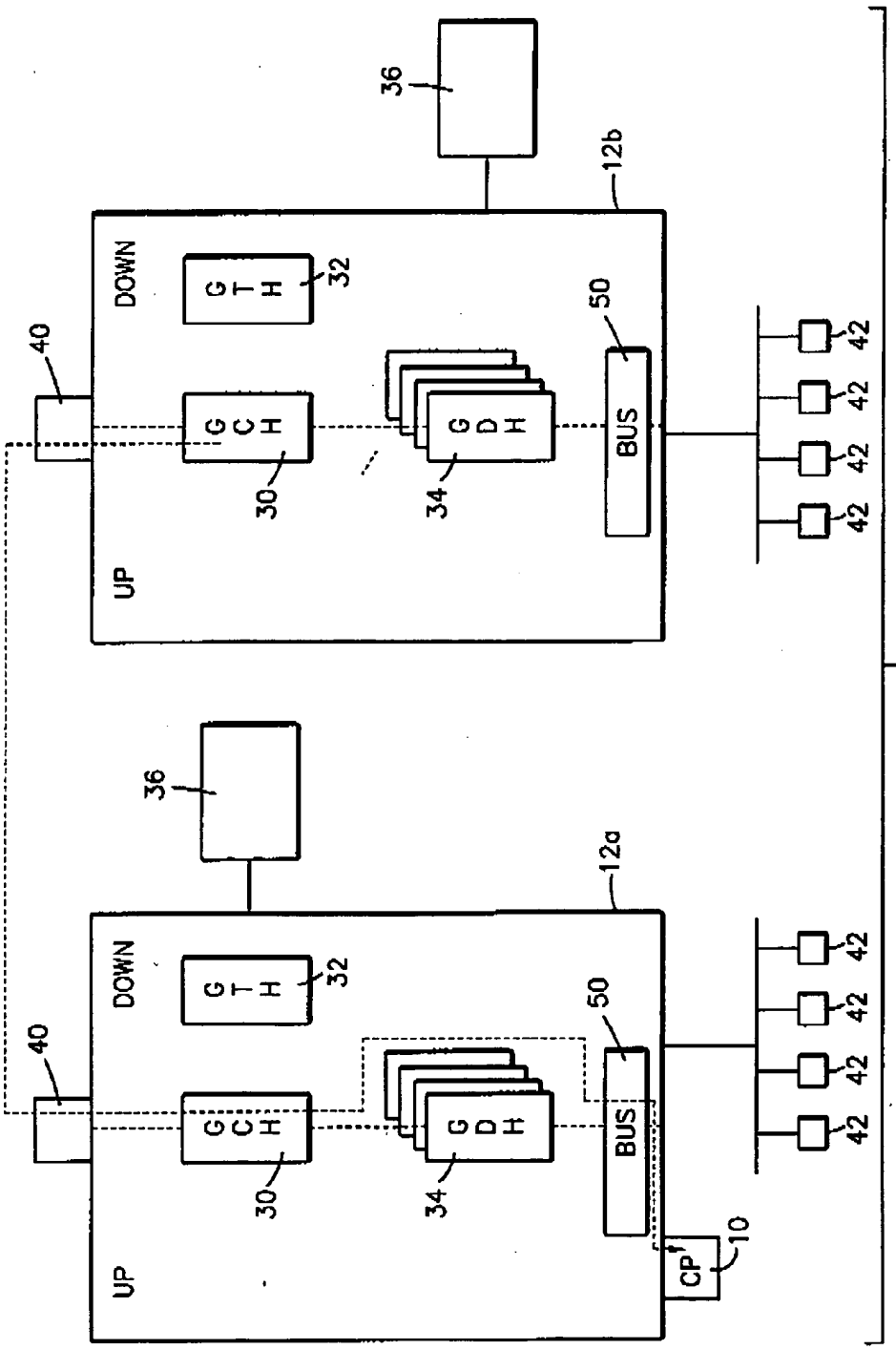
42
30
34
UP
36
12a
DOWN
GTH
32
50
40
GCH
GDH
BUS
42
42
42
42
30
34
CP
10
UP

Fig.9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GTH
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GCH
GDH
BUS
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UP
36
12a
DOWN
GTH
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GCH
GDH
BUS
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UP

Fig.10

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BASS ET AL
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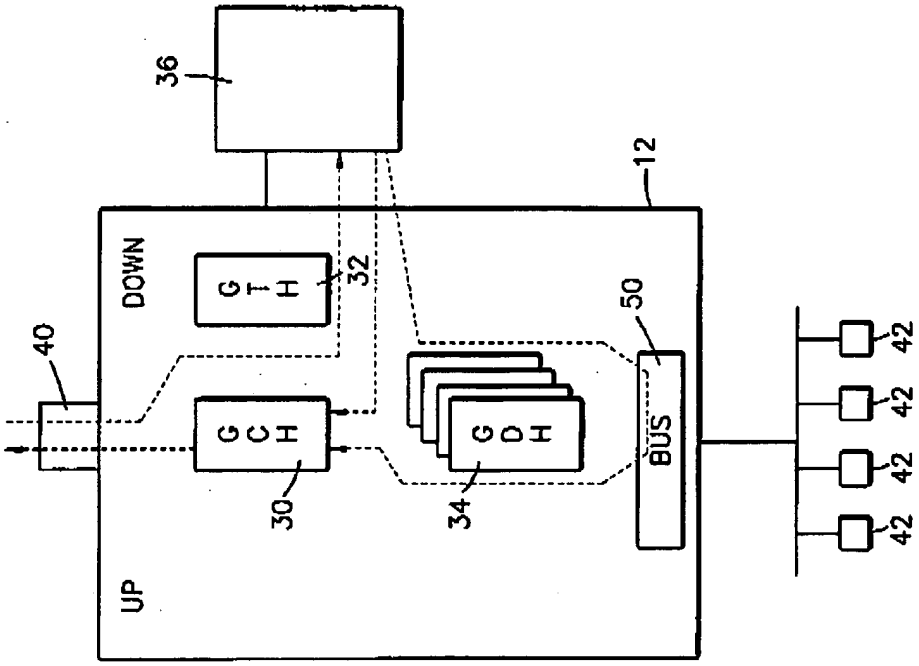


Fig.12

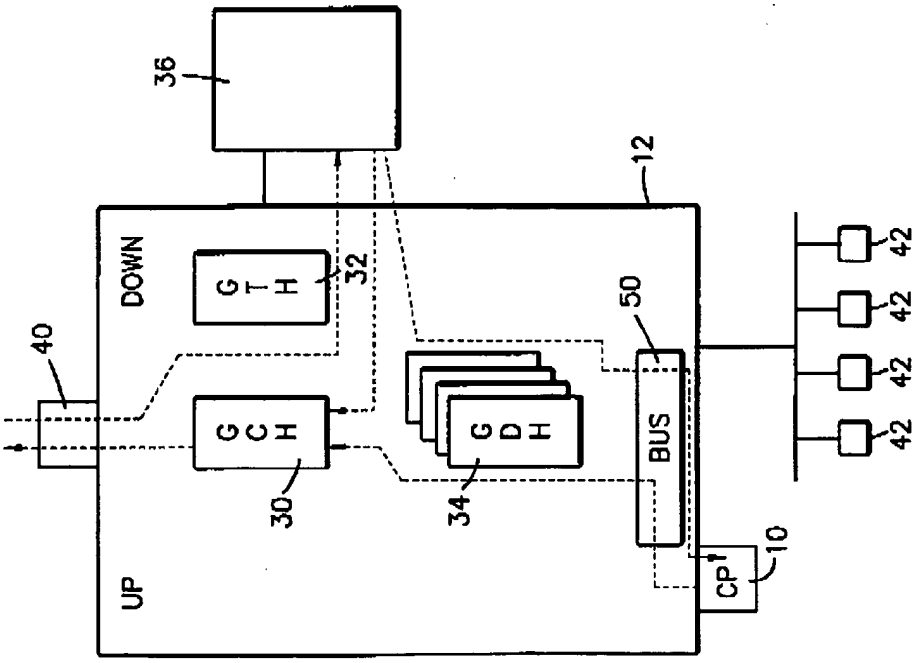
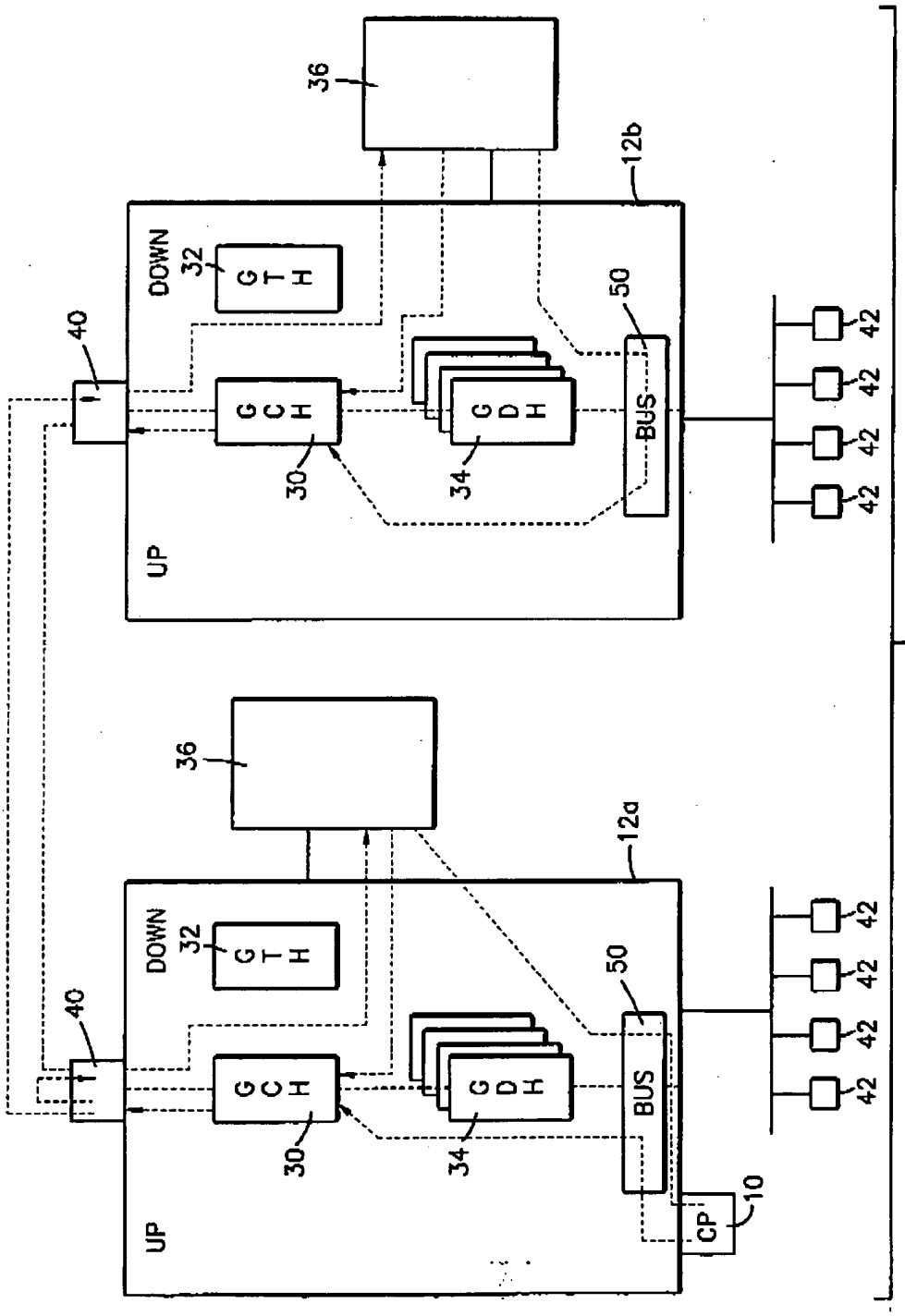


Fig.11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12b
DOWN
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G T H
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G C H
G D H
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BUS
42
42
42
42
30
34
UP
36
12a
DOWN
32
G T H
40
G C H
G D H
50
BUS
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42
30
34
UP
CP
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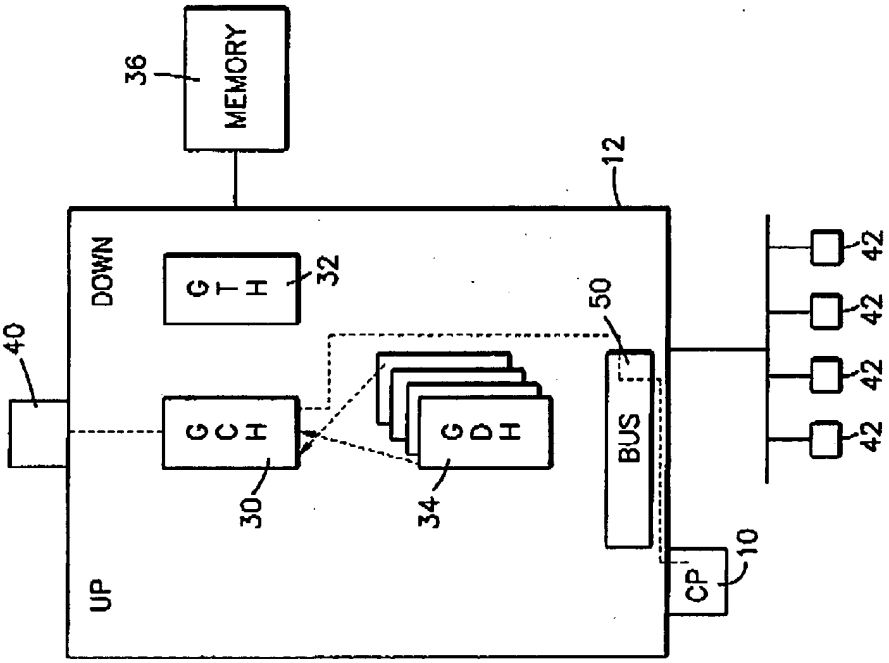
Fig.13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MEMORY
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G T H
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G C H
G D H
BUS
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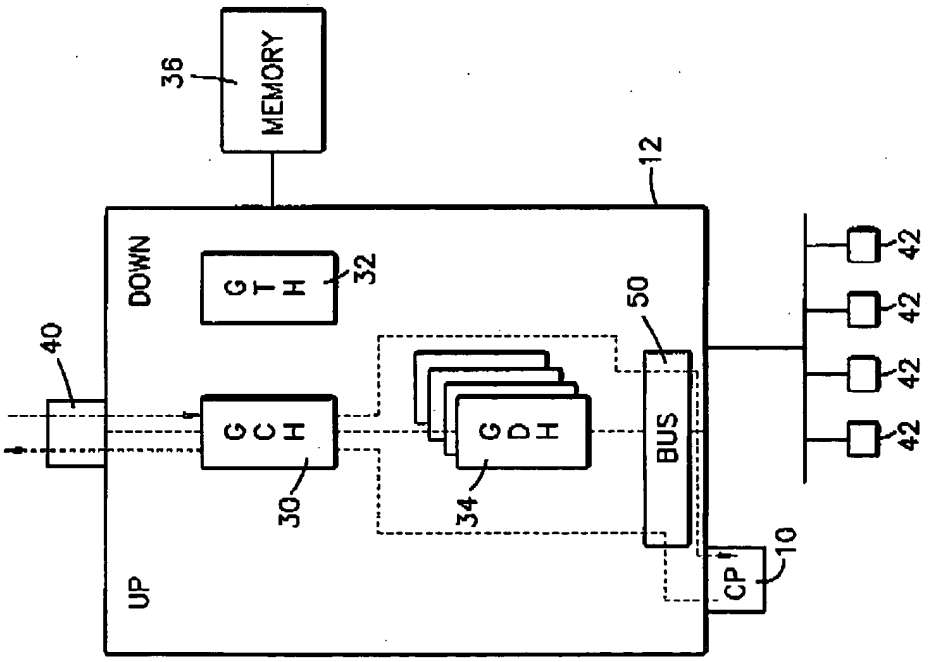
34
CP'
10
UP

Fig.15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MEMORY
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G T H
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G C H
G D H
BUS
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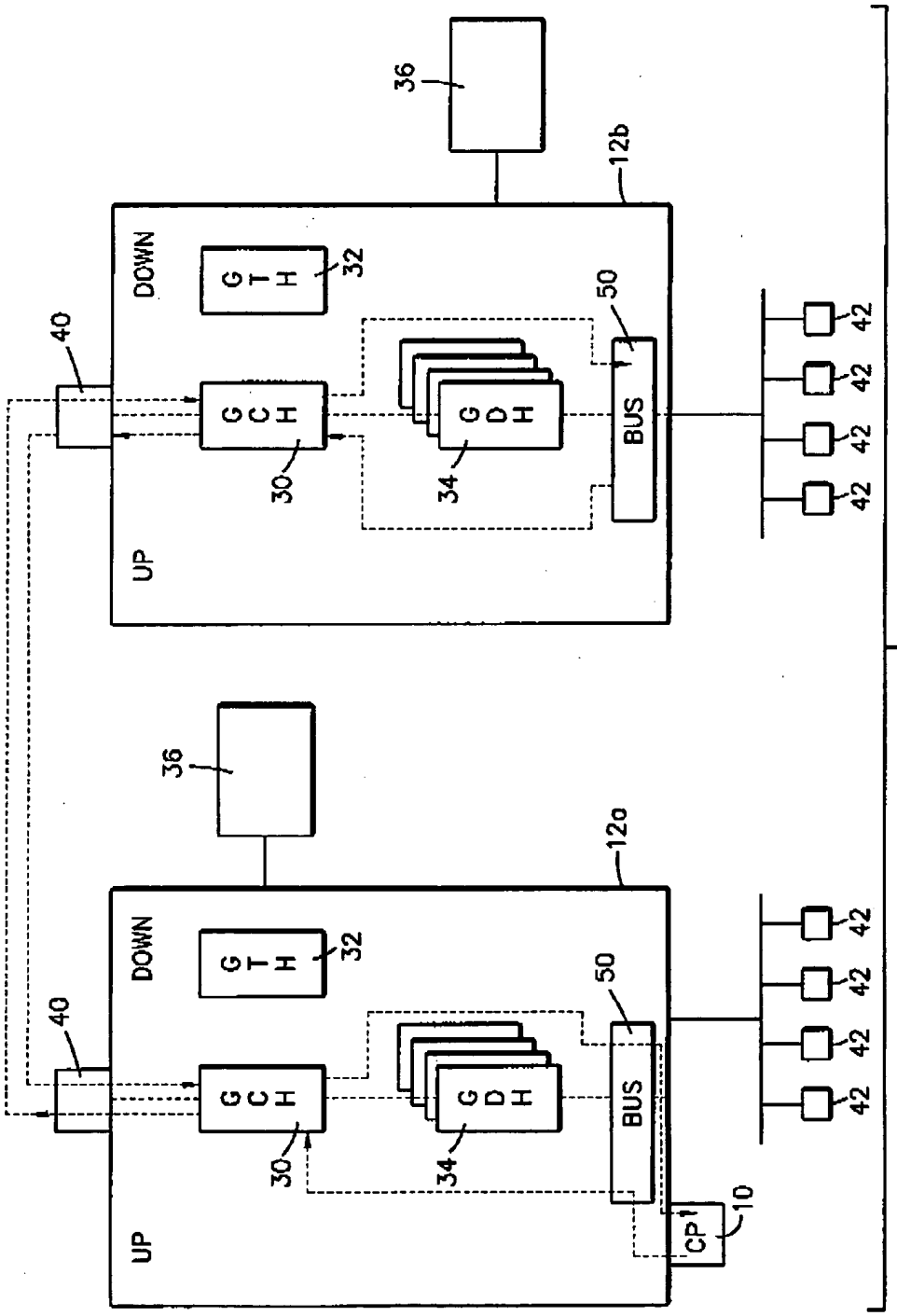
CP'
10
UP

Fig.14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12b
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GTH
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GCH
GDH
BUS
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30
34
UP
36
12a
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GDH
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UP
CP
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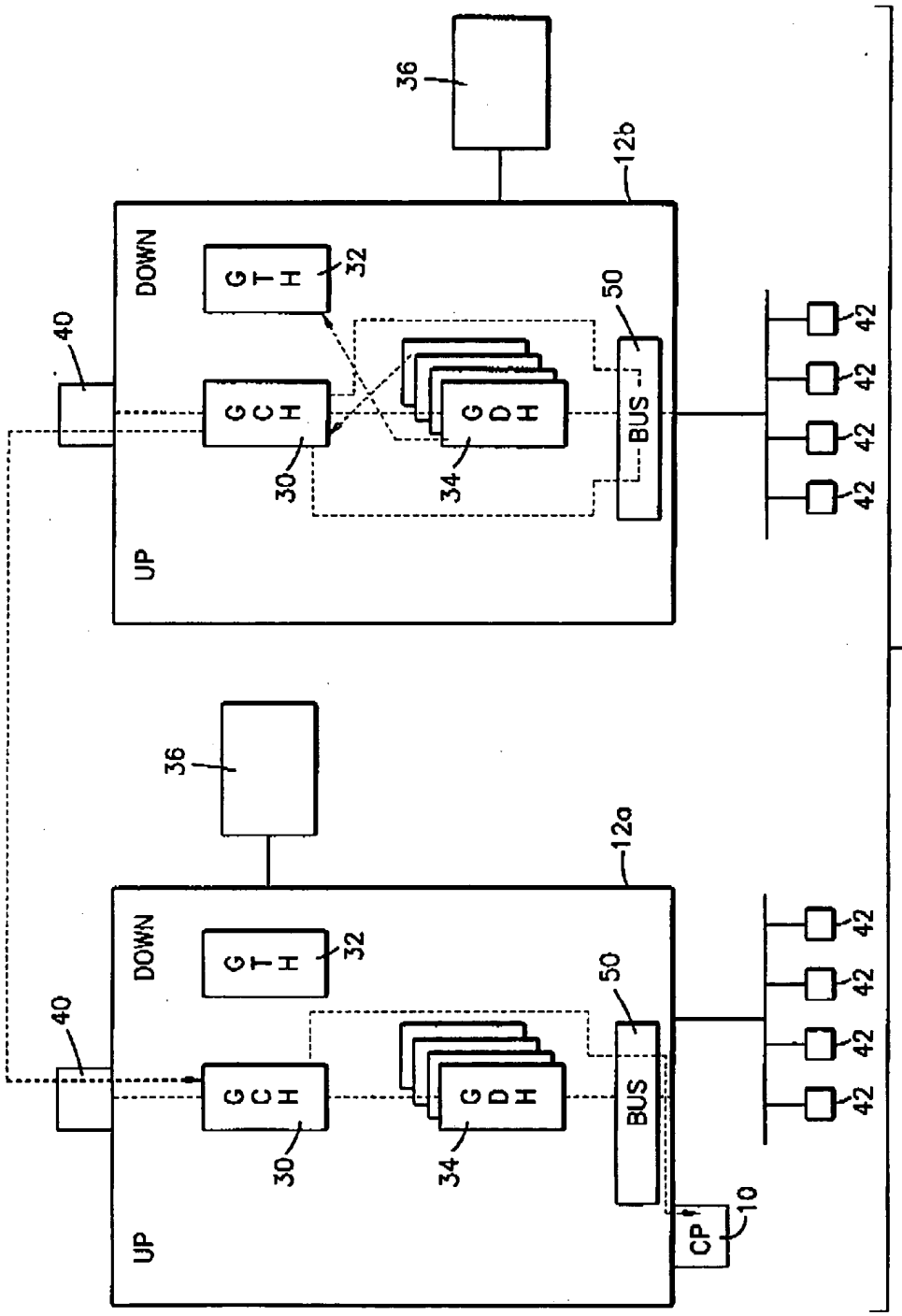
Fig.16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UP
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12a
DOWN
GTH
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GCH
GDH
BUS
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UP
CP
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Fig.17